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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 40301/487

In re patent application of

Allowed: December 7, 1999

Nobuo FUDANUKI et al.

Batch: F34

Serial No.: 08/997,035

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Examiner: D. Le

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE ARRAY  
AND STANDARD CELL




SUBMISSION OF FORMAL DRAWINGS

The Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicant submits herewith seventeen (17) sheets of formal drawings.

March 7, 2000  
Date

  
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